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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10042520	01/09/2002	438	386	2825	KESHAVAN, B

**APPLICANTS: Coronel Philippe; Piazza Marc; Leverd Francois;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:
FRANCE 0100691 01/18/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 00GR32554365
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Dram memory integration method		

U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
Amount Due		Sheets Drwg.	Figs. Drwg.
Date Paid		Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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